

~~processing sections is set to the first operation mode, and  $n-1$  of the image processing~~  
sections are set to the second operation mode;

wherein commands are commonly given to the  $n$  image processing sections  
from the control section; and

wherein, when a command is given from the control section to the one of  
the image processing sections that is set to the first operation mode, the  $n$  image processing  
sections individually execute the same processing with the same timing.

2. (Amended) The image-processing apparatus according to Claim 1, the  $n$   
image processing sections being allocated in the same address space in address spaces that  
can be controlled by the control section.

3. (Twice Amended) The image-processing apparatus according to Claim 1,  
each of the image processing sections including a mode-setting terminal that sets one of  
the first operation mode and the second operation mode, and one of the operation modes  
being set according to a mode-setting signal input to the mode-setting terminal.

4. (Twice Amended) The image-processing apparatus according to Claim 1,  
further including a memory that stores image-processing data commonly used by the  
respective image processing sections,

wherein the image processing section set to the first operation mode can  
write the image-processing data, which is fed from the control section, to the memory, and  
in addition, can read out the image-processing data written in the memory; and

wherein the image processing section set to the second operation mode can  
input the image-processing data read out by the image processing section set to the first  
operation mode from the memory.

5. (Twice Amended) An image-displaying apparatus, comprising:  
the image-processing apparatus according to Claim 1, and

~~an image-displaying section that displays images represented by video signals output from the image-processing apparatus.~~

6. (Amended) The image-processing apparatus according to Claim 2, each of the image processing sections including a mode-setting terminal that sets one of the first operation mode and the second operation mode, and one of the operation modes being set according to a mode-setting signal input to the mode-setting terminal.

7. (Amended) The image-processing apparatus according to Claim 2, further including a memory that stores image-processing data commonly used by the respective image processing sections,

wherein the image processing section set to the first operation mode can write the image-processing data, which is fed from the control section, to the memory, and in addition, can read out the image-processing data written in the memory; and

wherein the image processing section set to the second operation mode can input the image-processing data read out by the image processing section set to the first operation mode from the memory.

8. (Amended) The image-processing apparatus according to Claim 3, further including a memory that stores image-processing data commonly used by the respective image processing sections,

wherein the image processing section set to the first operation mode can write the image-processing data, which is fed from the control section, to the memory, and in addition, can read out the image-processing data written in the memory; and

wherein the image processing section set to the second operation mode can input the image-processing data read out by the image processing section set to the first operation mode from the memory.

9. (Amended) An image-displaying apparatus, comprising:  
the image-processing apparatus according to Claim 2, and  
an image displaying section that displays images represented by video  
signals output from the image-processing apparatus.
10. (Amended) An image-displaying apparatus, comprising:  
the image-processing apparatus according to Claim 3, and  
an image displaying section that displays images represented by video  
signals output from the image-processing apparatus.
11. (Amended) An image-displaying apparatus, comprising:  
the image-processing apparatus according to Claim 4, and  
an image displaying section that displays images represented by video  
signals output from the image-processing apparatus.

REMARKS

Claims 1-11 are pending. By this Supplemental Preliminary Amendment, the Abstract is replaced with a Substitute Abstract, and the specification and claims 1-11 are amended.

The attached Appendix includes marked-up copies of each rewritten paragraph (37 C.F.R. 1.121(b)(iii)) and claim (37 C.F.R. 1.121(c)(ii)).